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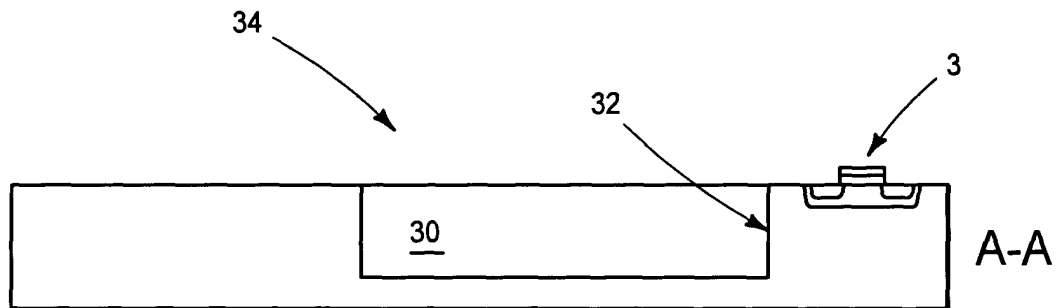


FIG. 3A

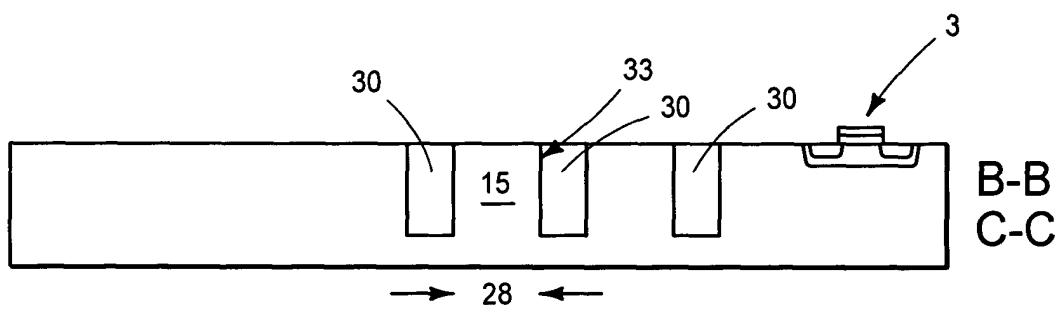


FIG. 3B

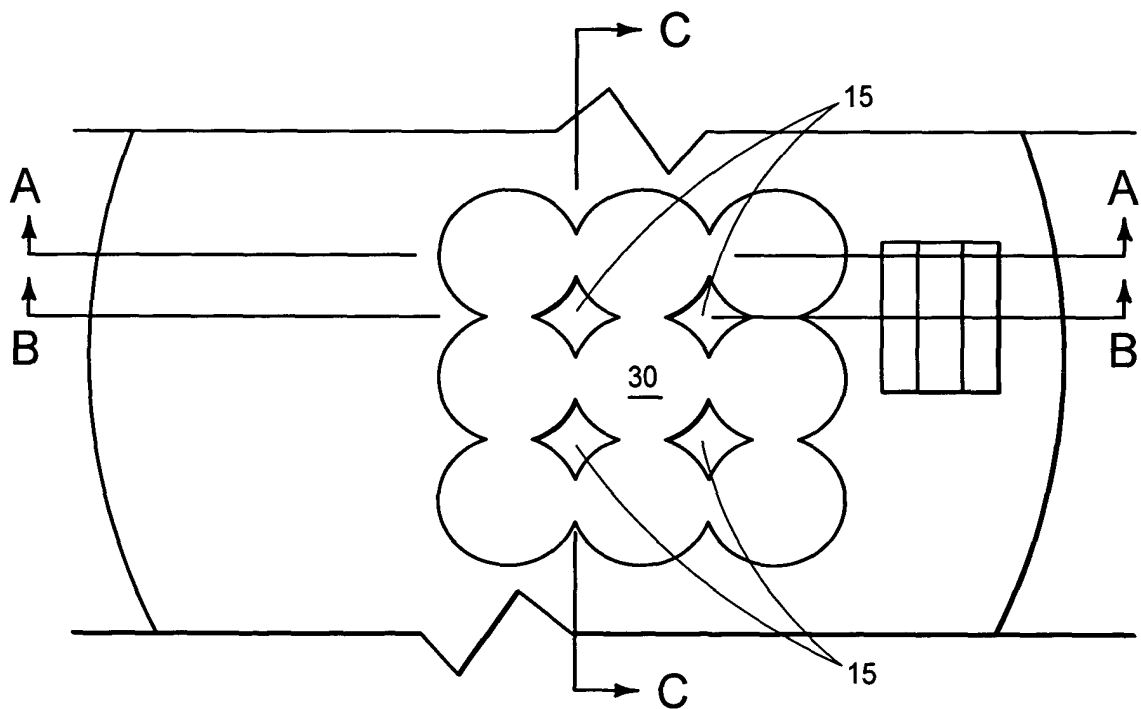


FIG. 4

TITLE: SEMICONDUCTOR DEVICE AND METHOD OF PROVIDING REGIONS OF  
LOW SUBSTRATE CAPACITANCE

INVENTOR: Durham et al.

DOCKET NO.: ONS00325



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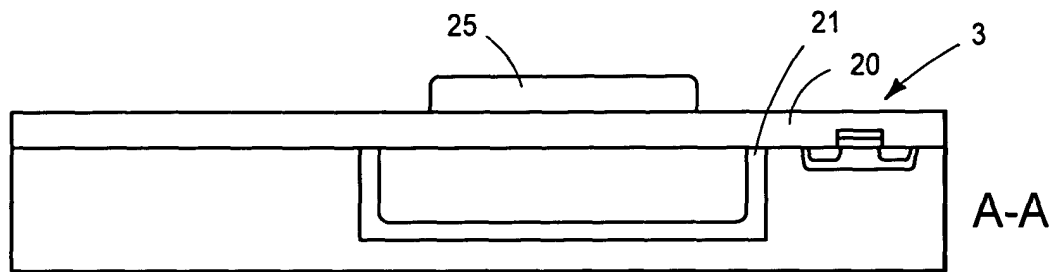


FIG. 5A

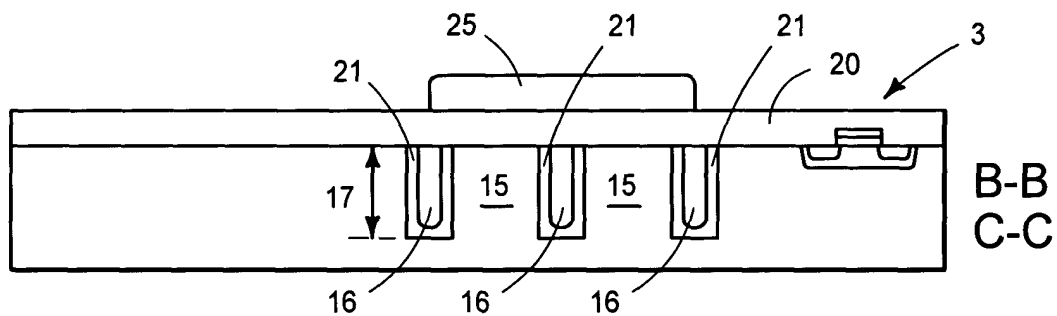


FIG. 5B

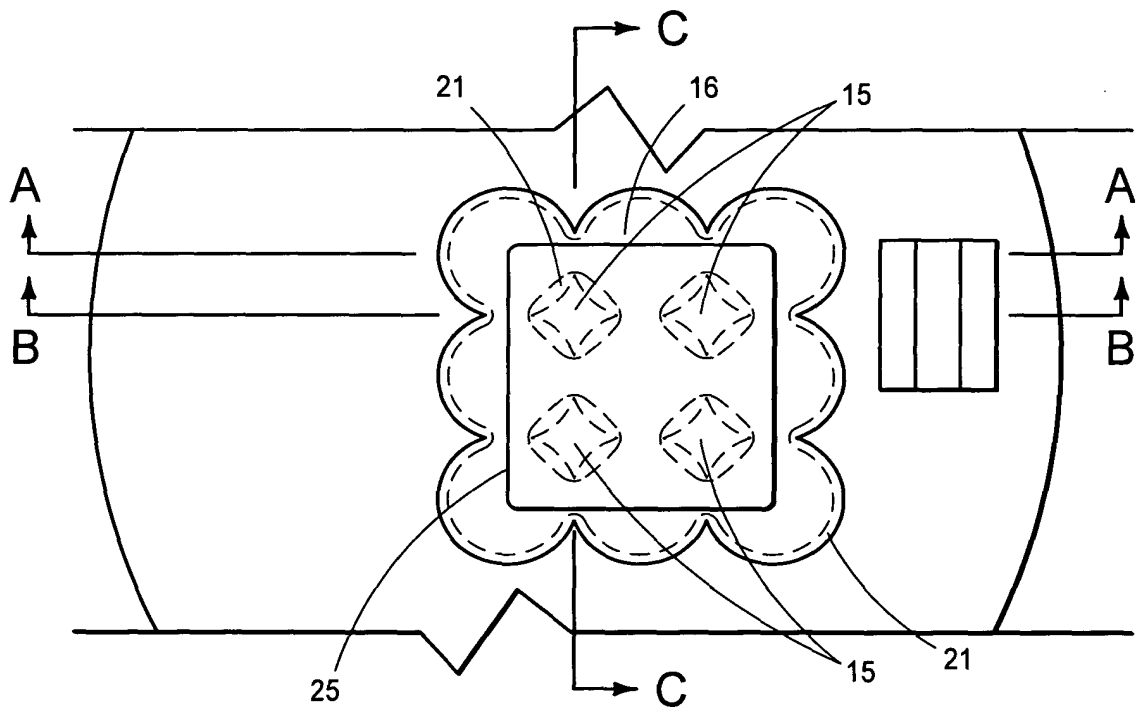


FIG. 6



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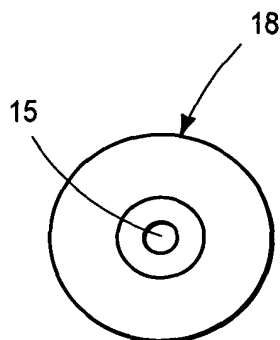


FIG. 7A

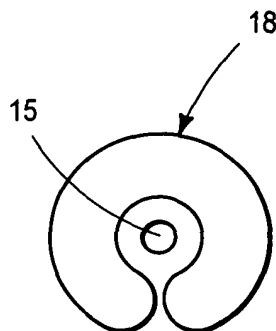


FIG. 7B

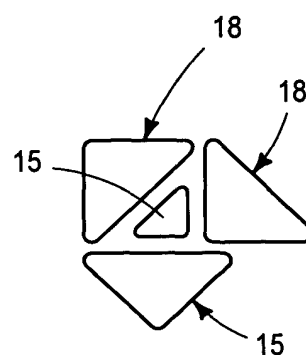


FIG. 7C

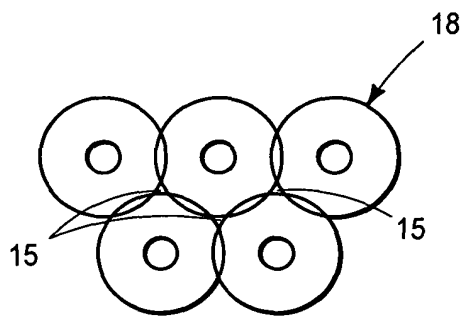


FIG. 7D

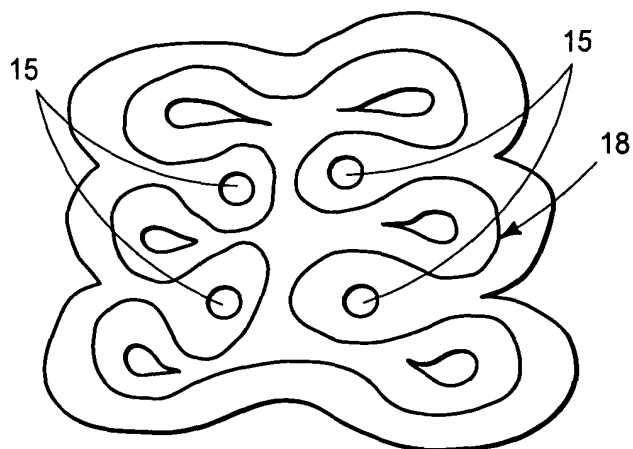


FIG. 7E